

**PROTECTION OF A PROGRAM WAITING TO BE EXECUTED IN A MEMORY USED BY
A MICROPROCESSOR**

ABSTRACT

5 A method for authorizing an access to a table of address correspondence between a multitask CPU and at least one memory containing several programs, consisting of calculating, on each task change of the CPU, a signature of at least part of the program instruction lines, and checking the conformity of this signature with a signature recorded upon previous execution of the involved program.